## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):

Dickinson, et al.

Docket No.:

LUX-P017

Serial No.:

(Not yet assigned)

Group Art Unit: (Not yet assigned)

Filed:

1/14/04

Examiner:

(Not yet assigned)

Title:

**Integrated Photonic-Electronic Circuits and Systems** 

## INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In keeping with the duty of candor and good faith owed to the Patent and Trademark Office, Applicants wish to bring the enclosed printed publications and U.S. patent references to the attention of the Examiner.

Applicant respectfully submits that Applicants' claimed invention is patentable over such references. Forms PTO/SB/08A and PTO/SB/08B listing these references are attached. It is understood the Office may find additionally relevant material in these or other references

Respectfully submitted,

Mircea Achiriloaie

Reg. No. 48,880

1/13/

Date

**CUSTOMER NO: 22877** 

FERNANDEZ & ASSOCIATES, LLP

**PATENT ATTORNEYS** 

PO BOX D

MENLO PARK, CA 94026-6204

PHONE: (650) 325-4999 FAX: (650) 325-1203 EMAIL: iploft@iploft.com

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PTO/SB/08A (10-86)

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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known			
Application Number	MOT YET ASSIGNED		
Filing Date	114/04		
First Named Inventor	Dickinson, et al		
Group Art Unit	NOT YET ASSIGNED		
Examiner Name	HOT YET ASSIGNED		
Attorney Docket Number	LUX - P017		

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				U.S. PATENT DOC	JMENTS	
Examiner Initials*	Cite No.1	Number (if kr	Code <sup>2</sup>	of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	_1	6,468,823	BI	scherer, et al	10/22/2002	
	2.	6.088.376		O'Biren, et al	7/11/2000	
	3.	6.649.439	BI	Nesnidal, et al	11/18/2003	
	4.	6,643, 439	BZ	Notomi, et al	11/04/2003	
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<sup>&</sup>lt;sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (MPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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Substitute for	r form 1449B/PTO		Complete If Known			
	_		Application Number	HOT YET ASSIGNED		
		DISCLOSURE	Filing Date	119/04		
STAT	EMENT BY	APPLICANT	First Named Inventor	DICKINSON, et al		
			Art Unit	NOT YET ASSIGNED		
	(Use as many sheet	s as necessary)	Examiner Name	NOT YET ASSIGNED		
Sheet	2	of 5	Attorney Docket Number	LUX-POIT.		

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	1.	"SCALING OPTOELECTRONIC - VLSI CIRCUITS INTO THE 21ST CENTURY: A TECHNOLOGY ROADMAP".	
		ASHOK V. KRISHNAMOORTHY, MEMBER, IEEE, AND DAVID A.B. MILLER, FELLOW IEEE; IEEE JOURNAL OF SELECTE	
		TOPICS IN QUATUM ELECTRONICS, VOL. 2, NO. 1, APRIL 1996	
	2.	"OPTICAL INTERCONNECTS TO STUTCON". DAVID A.B. MILLER THEE JOURNAL ON SELECTED TOPICS IN QUANTUM ELECTRONICS	
		VOL. 6. NO. G. NOVEMBER IDECEMBER 2000	•
	3,	"CHALLENGES IN OPTICALLY INTERCONNECTING ELECTRONICS FRANK A.P. TOOLEY, MEMBER, IEEE.	4
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	4.	"OPTICAL INTERCONNECTS: OUT OF THE BOX FOREVER,?"  DAWEI HUANG, MEMBER, IEEE, THERESA 3ZE, MEMBER,	
		IEEE ANDERS CANDIN, RICK LYTEL, MEMBER TEEE AND HOWARD L. DAVIOSON, SENIOR MEMBER, IEEE	

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Examiner Signature	Date	
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Substitute for form 1449B/PTO		Complete If Known
	Application Number	NOT YET ASSIGNED
INFORMATION DISCLOSURE	Filing Date	1/14/04
STATEMENT BY APPLICANT	First Named Inventor	Dickinson et al
	Art Unit	Not Yet Assigned
(Use as many sheets as necessary)	Examiner Name	Not yet Assigned
Sheet 3 of 5	Altomey Docket Number	Lux - P017

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	5_	"VERY SHORT REACH OPTICAL INTERCONNECTS IN SYSTEMS: THE CASE FOR FIBER-TO-THE-PROCESSOR".	
		The University of Southern California; http://www.usc.edu/alevi; AFJ. Levi; OPTICAL DIGITAL COMMUNICATION SEMINAR	
••••	******************************	ON MAY 21, 2001.	
	6.	"HIGH-SPEED OPTOELECTRONIC VLSI SWITCHING CHIP WITH >4000 OPTICAL I/O BASED ON FLIP-CHIP BONDING OF	
	•••••	MQW MODULATORS AND DETECTORS TO SILICON CMOS!"  Anthony L. Lentine, Keith W. Goossen, J. A. Walker leo,	•••••
	•	Arthur D'Asaro, P. Hui, B. J. Tseng, R. E. Loibenguth, J. E. Cunmingham, W. Y. Jan, Jen-Ming Kuo, D. W.	**********
		Dahringer, D. F. Kossives	***********
	7.	HETEROGENEOUS INTEGRATION OF OF ARRAYS WITH STELECTRONICS AND MICROOPTICS; YUE LIU; IEEE TRANSACTIONS ON	••••••
		ADVANCED PACKAGING, VOL. 25. NO 1. FEBRUARY ZOOZ.	•
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Examiner Signature	Date Considered	

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Substitute for form 1449B/PTO	Complete if Known		
	Application Number	NOT YET ASSIGNED	
INFORMATION DISCLOSURE	Filing Date	1/14/04	
STATEMENT BY APPLICANT	First Named Inventor	Dickinson, et al	
	Art Unit	Not Yet Assigned	
(Use as many sheets as necessary)	Examiner Name	Not Yel Assignal	
Sheet 4 of 6	Attorney Docket Number	LUX-7017.	

	<b>,</b>	NON PATENT LITERATURE DOCUMENTS					
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	8.	"FIBER WEIGHED FOR CHIP INTERCONNECT", WWW. CMPNET. COM. BY ANTHONY CATALDO, OCT 12, 2001;					
		URL: http://www.eetimes.com/story/OEG 2001101250061					
	9.	TECHNOLOGY BY JANICE ONANIAN MCMAHON'S TOM EMBERLEY'S					
		The court disposition is a fact that	•••••••				
	10.	"PROSPECTS OF CMOS TECHNOLOGY FOR HIGH SPEED OPTICAL COMMUNICATION CIRCUIT". BEH ZAD RAZAVI, SENIOR MEMBER,	•				
		IEEE; IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 37, NO. 9. SEPTEMBER 2002.	•••••				
	ħ.	B. JALALI, S. YEGNANA AAYANAW, T. YOON, T. YOSHIMOTO,					
		I. RENDINA, AND F. COPPINGER.  IEEE JOURNAL ON SELECTED TOPICS IN QUANTUM ELECTRONICS	•••••				
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Application Number

Substitute for form 1449B/PTO

INFORMATION DISCLOSURE			CHDE	Filing Date				
STATEMENT BY APPLICANT			1/14/04					
		First Named Inventor	Dickinson, et al					
			Art Unit	Not yet Assigned Not yet Assigned				
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Sheet 5. of 5					Attorney Docket Number	LUX-7017.		
				NON PATE	NT LITERATURE DOCUMENTS			
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	12.	"INTEGRATED OPTICS IN SILICON AND SIGE-HETEROSTRUCT Schüppert, et al 3 JOURNAL OF LIGHTWAVE TECHNOLOGY VOL. 14 NO 10 OCTORED 1991						
		VOL. 14. 1	NO. 10	, OCTOB	ER 1996 .			
13. Almeida, et al;		FOR COI	MPACT MODE C TICS LETTERS	ONVERSION"; VOL. 28 NO.15/AUC 1,03				
14. MEMBRANE-TYPE PH	PHOTONIC INTE OTONIC CRYSTA	GRATED CIRCUIT WITH L'WAVEGUIDEC"						
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